

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re The Application of: Myron J. Miske et al.)
Wiyion 3. Wiske et al.) Examiner: Mai, Lam T
Serial No.: 10/602,798)
Eilade Juna 24, 2002) Art Unit: 2819
Filed: June 24, 2003) Art Ollit. 2019
For: BUS HOLD CIRCUIT WITH)
POWER-DOWN AND OVER-	
VOLTAGE TOLERANCE	
	Cesari and McKenna, LLP
	88 Black Falcon Avenue
	Boston, MA 02210
	September 3, 2004

CERTIFICATE OF MAILING

I hereby certify that the following paper is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on September 3, 2004.

Elaine Cruz

Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231

Sir:

CHANGE OF CORRESPONDENCE ADDRESS

Pursuant to 37 C.F.R. §1.363, we request that the fee address for the above-identified patent be changed to:

Stephen Schott, Esq.
Fairchild Semiconductor Corporation
82 Running Hill Road
South Portland, Maine 04106

Please charge any additional fee occasioned by this paper to our Deposit Account

No. 03-1237.

Respectfully submitted,

Edwin H. Paul, Esq.

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